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Application Number	10/409,569
Filing Date	April 08, 2003
First Named Inventor	Solomon, Richard L.
Title	Methods and Systems for Improving
Art Unit	2112
Examiner Name	Patel, Nimesh G.
Attorney Docket Number	02-6256

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Assignee of record of the Statement under 37 CF	ne entire interest. See 37 CFR FR 3.73(b) is enclosed. (Form	R 3.71. <i>PTO/SB/96)</i>					
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Name Timothy					Telephone	(408) 433-7625	
Title and Company Senior Corporate Counsel, IP/Assistant Corporate Secretary, LSI Logic Corporation							
NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below.							
*Total of 1	orms are submitted.					the mode union to so file /ned b	

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AT STRA	STATEMENT UNDER 37 CFR 3.73(b)	
GTRA	Applicant/Patent Owner: LSI Logic Corporation	
•	Application No./Patent No.: 10/409,569 Filed/Issue Date: April 08, 2003	
	Entitled: Methods and Systems for Improving Delayed Read Handling	
	LSI Logic Corporation , a Corporation (Name of Assignee, e.g., corporation, partnership,	university, government agency, etc.)
	states that it is:  1.  the assignee of the entire right, title, and interest, or	
	an assignee of less than the entire right, title and interest     (The extent (by percentage) of its ownership interest is%)	
	in the patent application/patent identified above by virtue of either.	
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	The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.	& Novoc
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